# Compiler and Linker Output Analysis

Compiler and Linker Output Analysis ensure that the Compiler and Linker generate the correct response to non-referenced data variables, functions. The steps followed to perform Compiler and Linker Output Analysis for Software under test is listed in below section:

1. Check out the latest Source file, Header file, Make file, LD from the Bitbucket.
2. Create a new C project in eclipse IDE.
3. Save the project and build the Software (Refer Software Build Procedure present in the MGT-UH60-BP-S001[BP]) and note the compiler and linker warnings if any.
4. Introduce Linker errors manually and specific errors shall be monitored.

The details of compiler and linker switches used are available in MGT-UH60-S002[SDP].

# Analysis performed for Compiler and Linker Output

Compiler and Linker Output Analysis for Gateway Module is performed for the Baseline: EDAU\_SOI3\_RELEASE\_03

## H108E-856

Source code is checked out from the BItbucket path: <https://bitbucket.org/machglobaltech-sw/edau/Software/SourceCode/Release/EDAU_SOI3_RELEASE_03/Gateway/H108E-856>

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| **Sl No:** | **Software build procedure for Gateway Application Software(** **H108E-856 )** | **Compiler and Linker Output Analysis** |
| 1 | 18:21:14 \*\*\*\* Build of configuration Default for project H108E-856 \*\*\*\*  cs-make all  cs-rm.exe -f -r .\Debug  cs-rm.exe -f -r .\Release  mkdir .\Release  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwycrt0.c -o .\Release/daugwycrt0.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyinit.c -o .\Release/daugwyinit.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyhw.c -o .\Release/daugwyhw.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwymain.c -o .\Release/daugwymain.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyiadc.c -o .\Release/daugwyiadc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwytmr.c -o .\Release/daugwytmr.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyrs232.c -o .\Release/daugwyrs232.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyxram.c -o .\Release/daugwyxram.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwydtoa.c -o .\Release/daugwydtoa.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwytach.c -o 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arm-none-eabi-as -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard daugwyoscpua.s -o .\Release/daugwyoscpua.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyoscpu.c -o .\Release/daugwyoscpu.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyucos.c -o .\Release/daugwyucos.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyrevno.c -o .\Release/daugwyrevno.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyapp.c -o .\Release/daugwyapp.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyarinc825.c -o .\Release/daugwyarinc825.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyarinc8252.c -o .\Release/daugwyarinc8252.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwya825comm.c -o .\Release/daugwya825comm.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D 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arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyqueue.c -o .\Release/daugwyqueue.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwycomm.c -o .\Release/daugwycomm.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwybittest.c -o .\Release/daugwybittest.o  arm-none-eabi-as -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard daugwybittesta.s -o .\Release/daugwybittesta.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwycbit.c -o .\Release/daugwycbit.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyetitimer.c -o .\Release/daugwyetitimer.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwycrc.c -o .\Release/daugwycrc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwylogic.c -o .\Release/daugwylogic.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwylogic2.c -o .\Release/daugwylogic2.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwylogic2disc.c -o .\Release/daugwylogic2disc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwylogic3.c -o .\Release/daugwylogic3.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwysound.c -o .\Release/daugwysound.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c daugwyapplogic.c -o .\Release/daugwyapplogic.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D 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.\Release/daulibmisc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxadc.c -o .\Release/daulibstm32f4xxadc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxcrc.c -o .\Release/daulibstm32f4xxcrc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxdma.c -o .\Release/daulibstm32f4xxdma.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxflash.c -o .\Release/daulibstm32f4xxflash.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxfsmc.c -o .\Release/daulibstm32f4xxfsmc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxgpio.c -o .\Release/daulibstm32f4xxgpio.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxiwdg.c -o .\Release/daulibstm32f4xxiwdg.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxpwr.c -o .\Release/daulibstm32f4xxpwr.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxrcc.c -o .\Release/daulibstm32f4xxrcc.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxtim.c -o .\Release/daulibstm32f4xxtim.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxcan.c -o .\Release/daulibstm32f4xxcan.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxdac.c -o .\Release/daulibstm32f4xxdac.o  arm-none-eabi-gcc -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -c ./st\_lib/src/daulibstm32f4xxusart.c -o .\Release/daulibstm32f4xxusart.o  arm-none-eabi-gcc -Wl,-TH108E-856.ld -DDISABLE\_LIM\_OTHERS -O0 -I . -I ./st\_lib/inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -Wl,--fatal-warnings -Wl,--sort-section=alignment -Wl,--error-unresolved-symbols -Wl,-M -Wl,--no-gc-sections .\Release/daugwycrt0.o .\Release/daugwyinit.o .\Release/daugwyhw.o .\Release/daugwymain.o .\Release/daugwyiadc.o .\Release/daugwytmr.o .\Release/daugwyrs232.o .\Release/daugwyxram.o .\Release/daugwydtoa.o .\Release/daugwytach.o .\Release/daugwysynchro.o .\Release/daugwyrterr.o .\Release/daugwywdog.o .\Release/daugwytbase.o .\Release/daugwyintr.o .\Release/daugwyoscpua.o .\Release/daugwyoscpu.o .\Release/daugwyucos.o .\Release/daugwyrevno.o .\Release/daugwyapp.o .\Release/daugwyarinc825.o .\Release/daugwyarinc8252.o .\Release/daugwya825comm.o .\Release/daugwya825comm2.o .\Release/daugwya429.o .\Release/daugwylookup.o .\Release/daugwyqueue.o .\Release/daugwycomm.o .\Release/daugwybittest.o .\Release/daugwybittesta.o .\Release/daugwycbit.o .\Release/daugwyetitimer.o .\Release/daugwycrc.o .\Release/daugwylogic.o .\Release/daugwylogic2.o .\Release/daugwylogic2disc.o .\Release/daugwylogic3.o .\Release/daugwysound.o .\Release/daugwyapplogic.o .\Release/daugwyexmon.o .\Release/daugwymontab.o .\Release/daulibmisc.o .\Release/daulibstm32f4xxadc.o .\Release/daulibstm32f4xxcrc.o .\Release/daulibstm32f4xxdma.o .\Release/daulibstm32f4xxflash.o .\Release/daulibstm32f4xxfsmc.o .\Release/daulibstm32f4xxgpio.o .\Release/daulibstm32f4xxiwdg.o .\Release/daulibstm32f4xxpwr.o .\Release/daulibstm32f4xxrcc.o .\Release/daulibstm32f4xxtim.o .\Release/daulibstm32f4xxcan.o .\Release/daulibstm32f4xxdac.o .\Release/daulibstm32f4xxusart.o -Wl,-o.\Release/H108E-856.axf -Wl,-Map=.\Release/H108E-856.map  arm-none-eabi-objcopy -O srec --srec-forceS3 .\Release/H108E-856.axf .\Release/H108E-856.mx  tools\STM\_SRECCRC.exe .\Release/H108E-856.mx 0x08020000 0x00040000 \  .\Release/H108E-856.s19  Howell Instruments, Inc - Copyright 2013  HSW0257 CRC32 Insert Utility for SREC, Rev 1.03  Start: 134348800 (0x8020000)  Length: 262144 (0x40000)  Source: ./Release/H108E-856.mx  Destination: ./Release/H108E-856.s19  CONVERSION COMPLETE  Volume in drive C is Windows-SSD  Volume Serial Number is 9C13-F330  Directory of C:\UH-60\Capitol\Init\_Build\H108E-856\Release  07/21/2025 06:21 PM 455,256 H108E-856.s19  1 File(s) 455,256 bytes  0 Dir(s) 826,307,801,088 bytes free  18:21:27 Build Finished (took 13s.159ms) | 0 Errors 0 Warnings are obtained  Hence, no analysis is performed. |

## H108E-857

Source code is checked out from the Bitbucket path: <https://bitbucket.org/machglobaltech-sw/edau/Software/SourceCode/Release/EDAU_SOI3_RELEASE_03/Gateway/H108E-857>

|  |  |  |
| --- | --- | --- |
| **Sl No:** | **Software build procedure for MCD Software(H108E-857)** | **Compiler and Linker Output Analysis** |
| 2 | 18:27:16 \*\*\*\* Build of configuration Default for project H108E-857 \*\*\*\*  cs-make all  cs-rm.exe -f -r .\Debug  cs-rm.exe -f -r .\Release  mkdir .\Release  arm-none-eabi-gcc -DSTRIP\_LIB\_FUNCTIONS -O0 -I . -I ./st\_lib/inc -I ./inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -Wdouble-promotion -Wextra -Waggregate-return -Wcast-qual -Wdisabled-optimization -Wfloat-equal -Wformat -Wformat-security -Wjump-misses-init -Winline -Winvalid-pch -Wunsafe-loop-optimizations -Wlogical-op -Wlong-long -Woverlength-strings -Wpacked-bitfield-compat -Wpointer-arith -Wredundant-decls -Wshadow -Wmissing-format-attribute -Wswitch-default -Wsystem-headers -Wundef -Wunsuffixed-float-constants -Wvla -Wwrite-strings -Wconversion -Wsign-conversion -c daugwyappdata.c -o .\Release/daugwyappdata.o  arm-none-eabi-gcc -Wl,-TH108E-857.ld -DSTRIP\_LIB\_FUNCTIONS -O0 -I . -I ./st\_lib/inc -I ./inc -nostartfiles -nostdlib -nodefaultlibs -mcpu=cortex-m4 -mthumb -mfpu=vfpv4 -mfloat-abi=hard -D PACK\_STRUCT\_END=\_\_attribute\(\(packed\)\) -D ALIGN\_STRUCT\_END=\_\_attribute\(\(aligned\(4\)\)\) -fno-builtin -ffreestanding -fsingle-precision-constant -Wa, -Wall -Werror -Wdouble-promotion -Wextra -Waggregate-return -Wcast-qual -Wdisabled-optimization -Wfloat-equal -Wformat -Wformat-security -Wjump-misses-init -Winline -Winvalid-pch -Wunsafe-loop-optimizations -Wlogical-op -Wlong-long -Woverlength-strings -Wpacked-bitfield-compat -Wpointer-arith -Wredundant-decls -Wshadow -Wmissing-format-attribute -Wswitch-default -Wsystem-headers -Wundef -Wunsuffixed-float-constants -Wvla -Wwrite-strings -Wconversion -Wsign-conversion -Wl,--fatal-warnings -Wl,--sort-section=alignment -Wl,--error-unresolved-symbols -Wl,-M -Wl,--no-gc-sections .\Release/daugwyappdata.o -Wl,-o.\Release/H108E-857.axf -Wl,-Map=.\Release/H108E-857.map  arm-none-eabi-objcopy -O srec --srec-forceS3 .\Release/H108E-857.axf .\Release/H108E-857.mx  tools\STM\_SRECCRC.exe .\Release/H108E-857.mx 0x080A0000 0x040000 .\Release/H108E-857.s19  Howell Instruments, Inc - Copyright 2013  HSW0257 CRC32 Insert Utility for SREC, Rev 1.03  Start: 134873088 (0x80A0000)  Length: 262144 (0x40000)  Source: ./Release/H108E-857.mx  Destination: ./Release/H108E-857.s19  CONVERSION COMPLETE  Volume in drive C is Windows-SSD  Volume Serial Number is 9C13-F330  Directory of C:\UH-60\Capitol\Init\_Build\H108E-857\Release  07/21/2025 06:27 PM 172,032 H108E-857.s19  1 File(s) 172,032 bytes  0 Dir(s) 826,314,866,688 bytes free  18:27:18 Build Finished (took 1s.725ms) | 0 Errors 0 Warnings are obtained  Hence, no analysis is performed. |